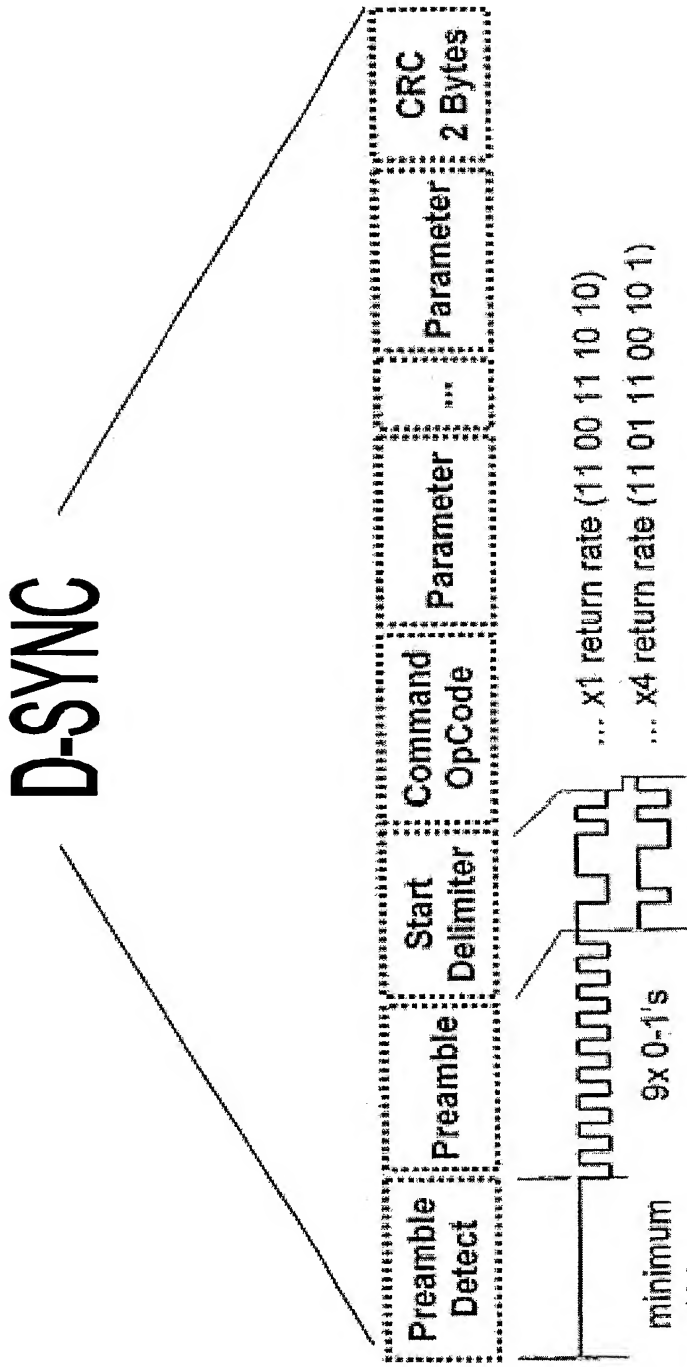
D-SYNC
Preamble Detect
Preamble
Start Delimiter
Command OpCode
Parameter
...
Parameter
CRC 2 Bytes
minimum 400 µs
9x 0-1's
... x1 return rate (11 00 11 10 10)
... x4 return rate (11 01 11 00 10 1)

Fig. 1

Command	Counter 0	1	2
Group Select XX	1 3 5 2 4		
Fail	2	1 4 3 5	
Success	1 4 3 5		
Fail	3 4	1 5	
Fail	4	3 5	1
Success	3 5	1	
Fail	5	3	1
Success	3	1	
Success	1		
Success	Reply		

Fig. 2

Fig. 3

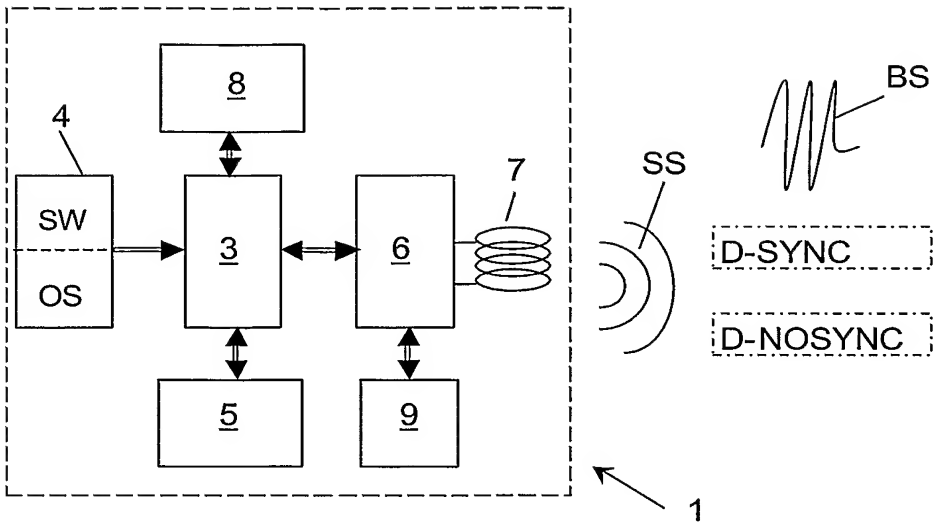
8
4
SW
OS
3
6
7
5
9
SS
BS
D-SYNC
D-NOSYNC
1

Fig. 4

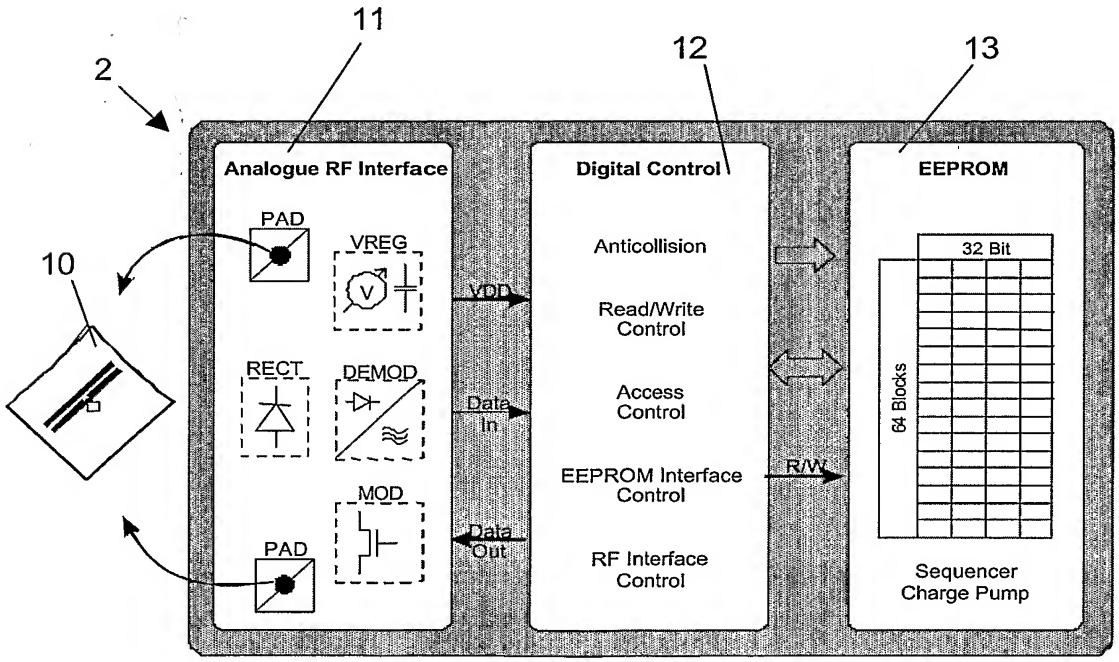
11
12
13
2
10
Analogue RF Interface
Digital Control
EEPROM
PAD
VREG
RECT
DEMOD
MOD
PAD
VDD
Data In
Data Out
Anticollision
Read/Write Control
Access Control
EEPROM Interface Control
RF Interface Control
R/W
32 Bit
64 Blocks
Sequencer Charge Pump

Fig. 5

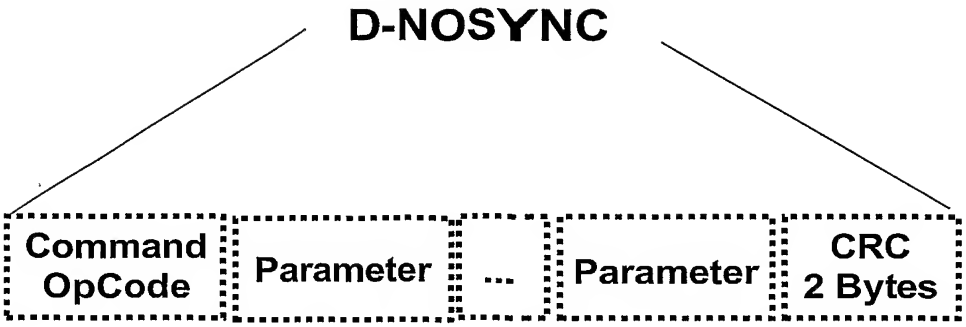

Fig. 6

Fig. 7

Fig. 8

Fig. 9

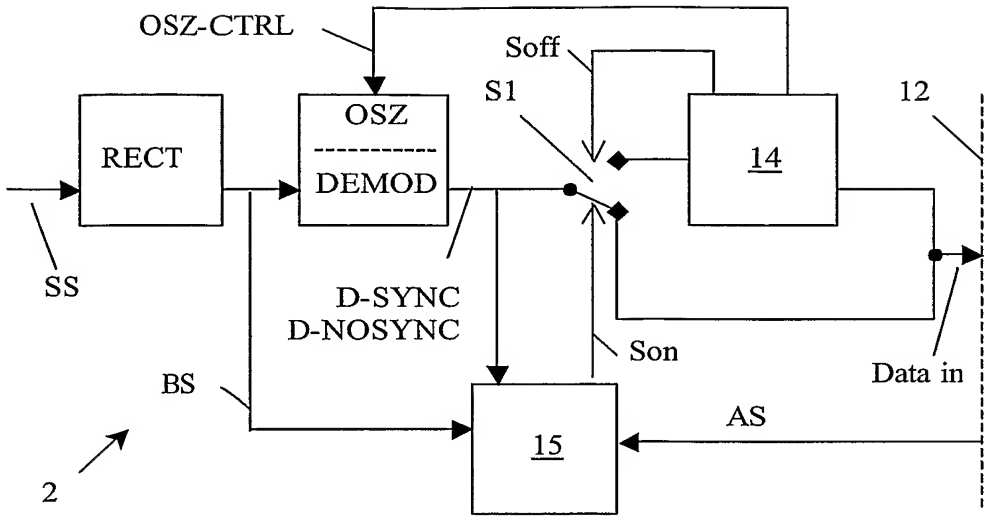
OSZ-CTRL
Soff
S1
12
RECT
OSZ
DEMOD
14
SS
D-SYNC
D-NOSYNC
Son
Data in
BS
AS
15
2

Fig. 10

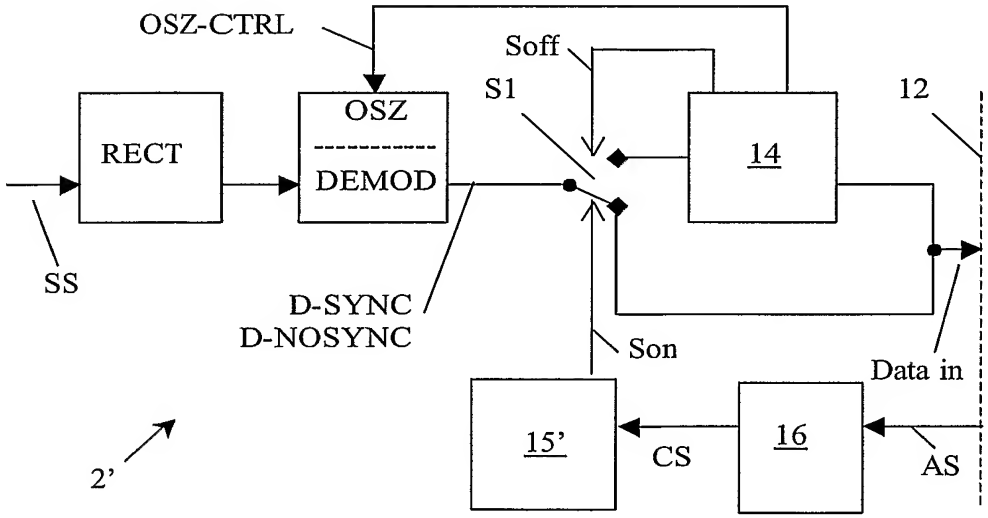
OSZ-CTRL
Soff
S1
12
RECT
OSZ
DEMOD
14
SS
D-SYNC
D-NOSYNC
Son
Data in
15'
CS
16
AS
2'

Fig. 11

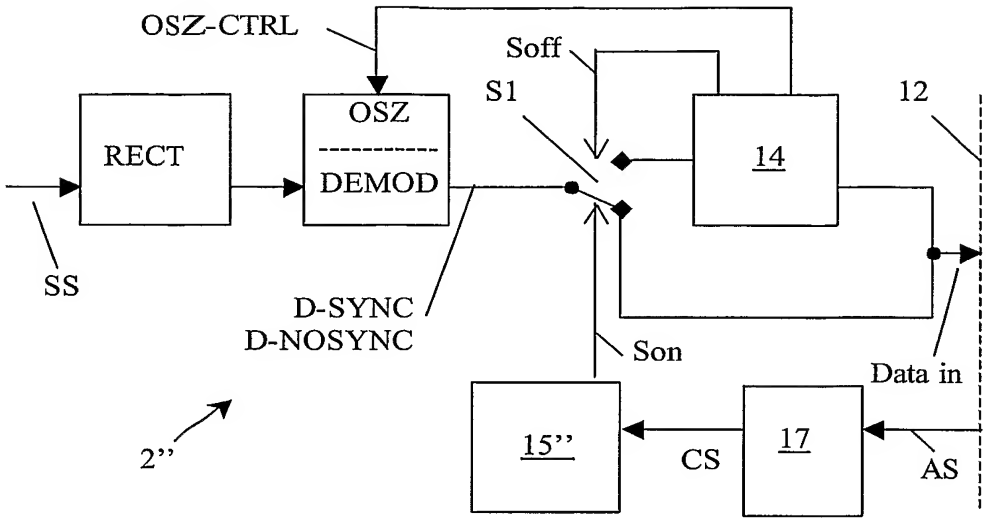
OSZ-CTRL
Soff
S1
12
RECT
OSZ
DEMOD
14
SS
D-SYNC
D-NOSYNC
Son
Data in
2''
15''
CS
17
AS

Fig. 12

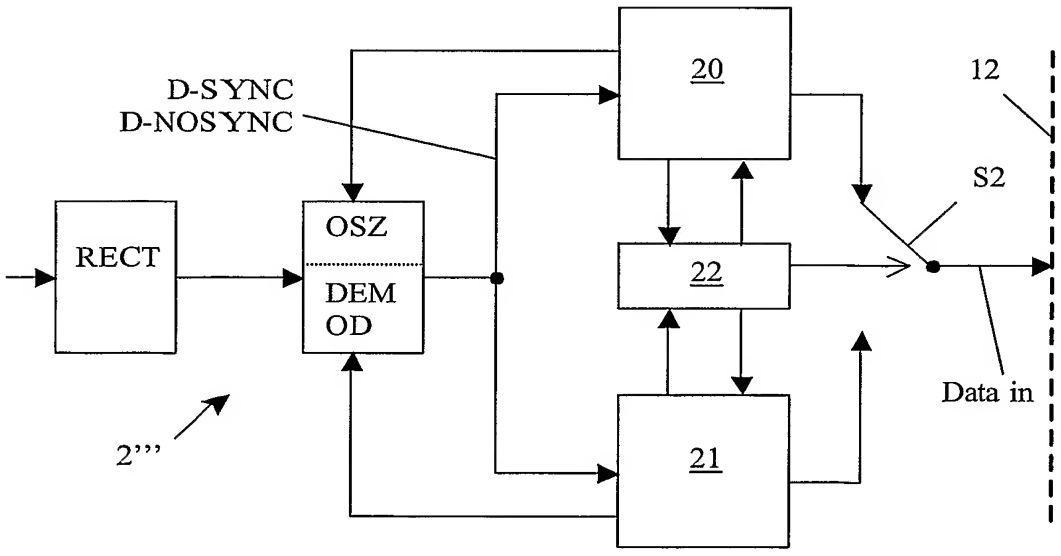
20
12
D-SYNC
D-NOSYNC
S2
RECT
OSZ
DEM
OD
22
Data in
2'''
21

Fig. 13